



LI DE HENG ELECTRONICS

SF11 thru SF17

1.0A Super Fast Recovery Rectifier

Rectifier Reverse Voltage 50 to 600V



DO-41

## Features

- Diffused junction
- Fast switching for high efficiency
- High current capability and low Forward Voltage Drop
- Surge overload rating to 30A peak
- Low reverse leakage current
- Plastic material has UL flammability classification 94V-0

## Mechanical Data

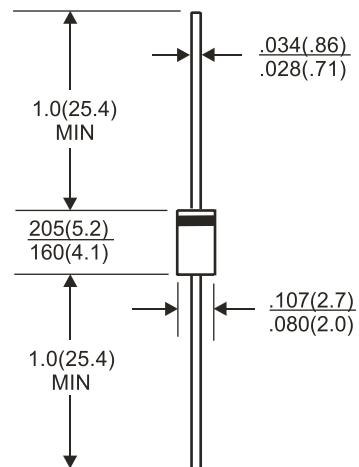
Case: Molded plastic

Terminals: Solder plated solderable per MIL-STD-202,  
Method 208

Polarity: Cathode band

Mounting Position: Any

Weight: 0.3grams (approx)



All dimensions inches and (millimeters)

## Maximum Ratings & Thermal Characteristics

Rating at 25°C ambient temperature unless otherwise specified, Resistive or Inductive load, 60 Hz.  
For Capacitive load derate current by 20%.

Parameter	Symbol	SF11	SF12	SF13	SF14	SF15	SF16	SF17	unit
Maximum repetitive peak reverse voltage	VRRM	50	100	150	200	300	400	600	V
Maximum RMS bridge input voltage	VRMS	35	70	105	140	210	280	420	V
Maximum DC blocking voltage	VDC	50	100	150	200	300	400	600	V
Maximum average forward rectified output current at TA=55°C	IF(AV)					1.0			A
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method)	IFSM					30.0			A
Maximum reverse recovery time TJ=25°C	Tr				35				nS
Typical thermal resistance per element	ReJA				50				°C/W
Typical junction capacitance per element	C <sub>j</sub>				50				pF
Operating junction and storage temperature range	T <sub>j</sub> , T <sub>STG</sub>				-55 to + 150				°C

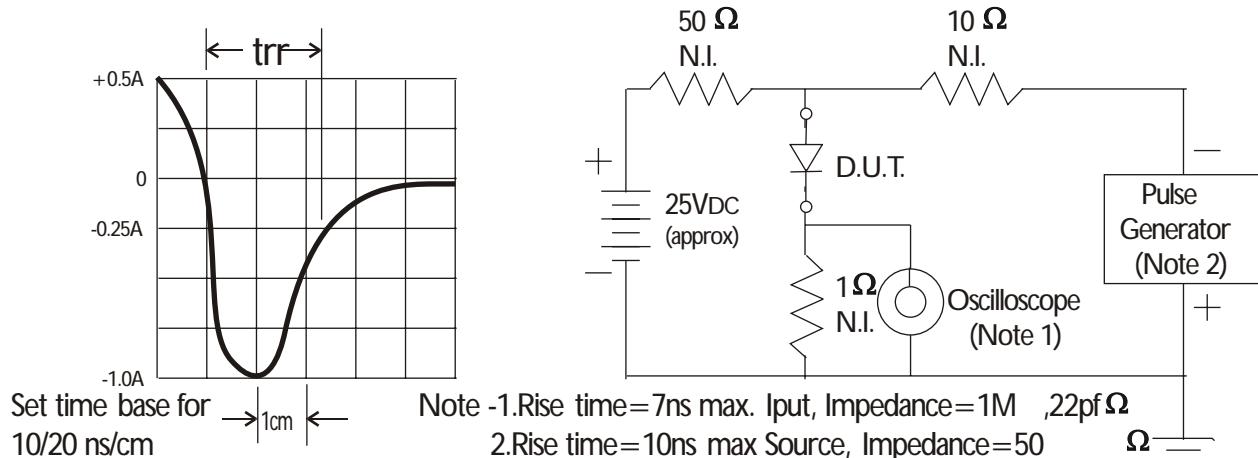
## Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified. Resistive or Inductive load, 60Hz.  
For Capacitive load derate by 20 %.

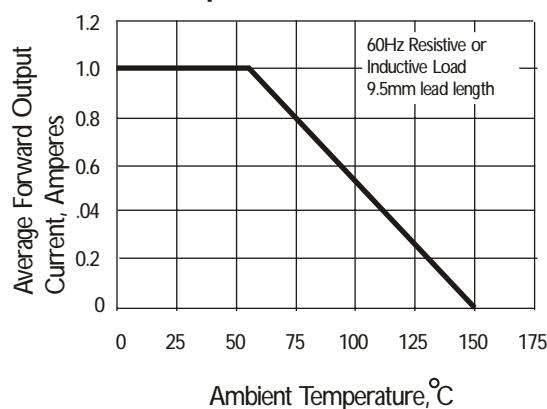
Parameter	Symbol	SF11	SF12	SF13	SF14	SF15	SF16	SF17	Unit
Maximum instantaneous forward voltage drop per leg at 1.0A	VF		0.95			1.25		1.50	V
Maximum DC reverse current at rated TA =25°C DC blocking voltage per element TA =125°C	IR			10	100				μA

**Rating and Characteristic Curves ( TA=25°C Unless otherwise noted )**  
**SF11 thru SF17**

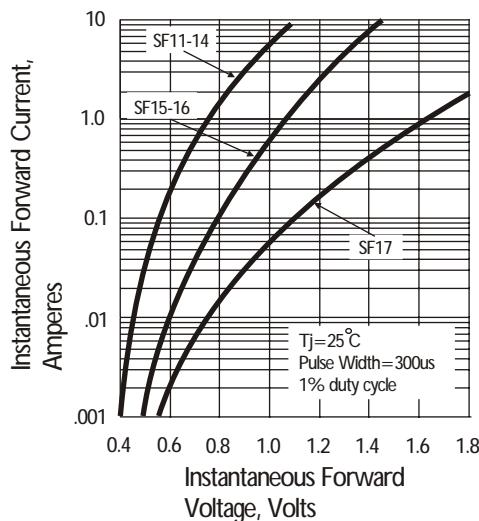
**Fig. 1 Reverse Recovery Time and Test Circuit Diagram**



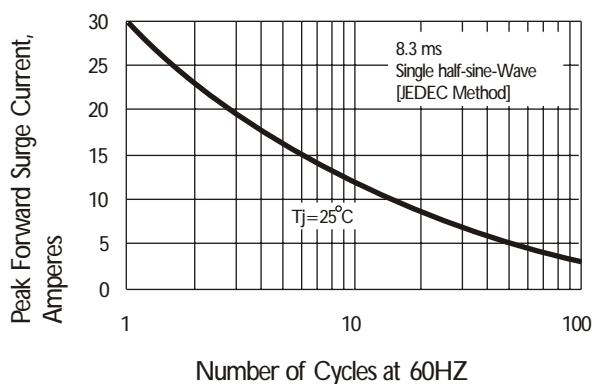
**Fig. 2 Derating Curve for Output Rectified Current**



**Fig. 4 Typical Instantaneous Forward Characteristics**



**Fig. 3 Peak Forward Surge Current**



**Fig. 5 Typical Junction Capacitance**

